Anisotropic conductivity of the Si(111)4×1-In surface: Transport mechanism determined by the temperature dependence

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The temperature dependence of anisotropic conductivity of a quasi-one-dimensional metallic surface, Si(111)4×1-In, was measured by a variable-temperature four-tip scanning tunneling microscope. Using the square four-point probe method, we succeeded in measuring the conductivity parallel and perpendicular to the In chains independently as a function of temperature. It was shown that the conductivity perpendicular to the In chains was mainly the conductivity of the space charge layer of the substrate. Moreover, it was clarified that it strongly depends on the substrate flashing temperature and this sometimes hindered the anisotropic conductivity at lower temperatures. In contrast, the conductivity parallel to In chains was clearly dominated by the surface states and decreased drastically around 110 K by the well-known 4×1 to 8×2 metal-insulator transition. The low temperature 8×2 phase had an energy gap as large as ~250 meV, consistent with previous photoemission reports.

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I. INTRODUCTION

One-dimensional (1D) electron systems have been an area of active research since they show a variety of unusual physical properties,¹ such as the Tomonaga-Luttinger liquid¹²,¹³, the spin-charge separation¹⁴, and the formation of charge- or spin-density waves (CDW or SDW) due to the Peierls instability.⁵,⁶ A Si(111)4×1-In surface superstructure, composed of a massive array of metallic In atomic chains, is known to have quasi 1D metallic surface-state bands⁸. It is found that a metal-insulator (MI) transition occurs in this system at about 120 K, where the 4×1 periodicity changes to the 8×2 phase.⁹ Initially it was suggested that the transition was driven by a (weak-coupling) Peierls instability.⁹ This was supported by other experimental evidences.¹⁰,¹¹ However, other groups challenged this explanation based on calculations insisting that this transition was rather an order-disorder type.¹²,¹³ This actually is within the framework of a strong-coupling CDW transition.¹⁵ A detailed angle-resolved photoemission spectroscopy (ARPES) study showed that the band dispersions changed abruptly at around 120 K, producing a relatively large energy gap at $E_F$ (~300 meV) compared to the energy scale of the transition temperature, which actually suggested that the CDW transition was not a weak-coupling type.¹⁶ It was found that the size of the gap showed a negligible temperature dependence, and proposed that the MI transition was caused by the cooperative effect of the Peierls and other structural transitions.¹⁶ A recent surface X-ray diffraction study proposed a very similar pseudo-first-order phase transition scenario.¹⁷ DFT calculations combined with reflection anisotropy spectroscopy also show that this system undergoes a quasi-Peierls distortion with phonon-softening.¹⁸,¹⁹ Thus, it seems that the origin of the MI transition in the quasi-1D indium chains on Si(111) surface is still a very intriguing topic and requests further experimental/theoretical studies for consistent understanding of the phase transition (weak- or strong-coupling CDW or others).

Despite the above intricate debate, the Si(111)4×1-In surface is rather simple and of course interesting in terms of surface-state transport. Clear evidence of the MI transition was found by temperature-dependent surface conductivity measurements using the linear micro four-point-probe method (m4PP).²⁰ Furthermore, the anisotropic conductivity reflecting the quasi-1D band dispersion was directly measured with a four-tip scanning tunneling microscope (STM)²¹ using the rotational square m4PP method.²² However there was one problem in the temperature dependent transport data. In the linear m4PP, the obtained conductivity ($\sigma$) is the geometric mean of the conductivity parallel ($\sigma_\parallel$) and perpendicular ($\sigma_\perp$) to the In chains ($\sigma = \sqrt{\sigma_\parallel \sigma_\perp}$). Since there is evidence that $\sigma_\parallel$ is the space-charge-layer contribution,²³ we need to measure the temperature dependence of $\sigma_\parallel$ and $\sigma_\perp$ independently and unravel the actual transport mechanism of each channel to precisely discuss the surface-state transport. The previous measurements were performed with a four-tip STM that had no cooling capabilities.²¹,²² But we have recently developed a new machine which has the capability to cool down the sample and the tips.²⁴ Therefore in this report, we have measured the conductivity of the Si(111)4×1-In surface using this machine with the square m4PP method and deduced the temperature dependence of $\sigma_\parallel$ and $\sigma_\perp$ independently. We found that the behavior of $\sigma_\perp$ could be explained by the space-charge layer conductivity and showed significant change when we changed the substrate flashing temperature. On the other hand, we found that $\sigma_\parallel$ was actually dominate by the surface-state conductivity as expected. It showed a MI transition around 110 K accompanying the 4×1 to 8×2 structural transition. The obtained gap size of the low temperature phase was ~250 meV, which was in accordance with that estimated
II. EXPERIMENTAL

The conductivity measurements were performed with our custom-made variable-temperature four-tip STM system, in which the sample and tips were cooled down to 7 K in ultrahigh vacuum (UHV). Each probe made of a tungsten tip can be independently driven with piezoelectric actuators and a scanner in the xyz directions to achieve precise positioning in nanometer scale under a scanning electron microscope (SEM). The four-tip probes can be made to contact the sample surface in arbitrary arrangements, with marginal damage by tunneling current approach and minute direct contact.

Vicinal Si wafers with $1 \sim 2^\circ$ miscut from the (111) axis were used to grow a single-domain $4 \times 1$ phase. We used two types of substrates ($n$-type substrate (P-doped) with a resistivity $\rho = 1 - 10 \ \Omega \cdot \text{cm}$ at room temperature (RT) and a non-doped substrate with a bulk resistivity $\rho \geq 1000 \ \Omega \cdot \text{cm}$ at RT) to study the effect of bulk conductivity on the measured conductivity. To obtain a single-domain $4 \times 1$ surface, a highly regular arrays of steps on Si(111) are needed, which can be formed by a multistep annealing sequence. In this sequence the Si substrate is heated up to 1250 °C in UHV. However, Zhang et al. showed that such high temperature flashing caused the formation of a p-type layer near the surface region. To avoid this, we also used a different way to clean the surface which was the Ishizaka-Shiraki method (chemical treating by HF etching in air and flashing up to 900 °C in UHV). The Si(111)$4 \times 1$-In surface was prepared by In deposition onto a cleaned Si(111)$7 \times 7$ surface at 450 °C. The structural formation was monitored in situ during deposition by reflection high-energy electron diffraction (RHEED) observation.

III. RESULTS AND DISCUSSION

A. Anisotropic conductivity measurements at RT by the square m4PP method

Figure 1(a) shows current-voltage ($IV$) curves of a single domain Si(111)$4 \times 1$-In surface measured by the square m4PP method. The probe spacing was 15 µm as shown by a SEM image at the left upper inset. The probes 1-4 are arranged in a square on the sample surface. The substrate for this data was flashed at 1250°C to obtain a clean $7 \times 7$ surface. The blue open squares show the data points for the chain perpendicular ($\perp$) direction ($R_{\perp} = V_{34}/I_{12}$), which means that the current flows between probes 1 and 2, and the voltage drop between probes 3 and 4 is measured) and the red filled circles are those for the chain parallel ($\parallel$) direction ($R_{\parallel} = V_{23}/I_{14}$). The obtained resistance which is the gradient of the $IV$ curves, is $R_{\parallel} = 250 \pm 20 \ \Omega$ and $R_{\perp} = 3.83 \pm 0.03 \ \text{k}\Omega$, respectively, showing a large anisotropy as in the previous report.

This resistance can be converted into the conductivity in the following way. The resistance of an infinite 2D layer/sheet measured in a square 4PP arrangement with equidistant probe spacing is given by

$$R_{\parallel} = \frac{1}{2\pi\sqrt{\sigma_{\parallel}/\sigma_{\perp}}} \ln \left(1 + \frac{\sigma_{\perp}}{\sigma_{\parallel}}\right)$$

(1)

$$R_{\perp} = \frac{1}{2\pi\sqrt{\sigma_{\parallel}/\sigma_{\perp}}} \ln \left(1 + \frac{\sigma_{\parallel}}{\sigma_{\perp}}\right)$$

(2)

where $\sigma_{\parallel}$ and $\sigma_{\perp}$ are the conductivities along and per-
an order of magnitude and decreased as the temperature near RT, there is no difference between \( \sigma \parallel \) and \( \sigma \perp \) below about 100 K. Furthermore, the anisotropy remained even at low temperatures. Thus, the measured conductivity depends very much on the flashing temperature for surface cleaning.

Although we did the same measurements with different substrates (n-type P-doped, bulk resistivity \( 1 \sim 10 \, \Omega \cdot \text{cm} \) at RT), the measured values were in almost the same order even though the bulk resistivity differed by three orders of magnitude. This proves that the bulk does not contribute to these results and only the surface states \((\sigma_{SS})\) and space charge layer \((\sigma_{SCL})\) conductivities are measured. Furthermore, the case of n-type substrate also showed similar features as the non-doped substrate; negligibly small anisotropy was shown at low temperature when flashed at 1250 \( ^\circ \text{C} \) while anisotropy was clearly detected for the sample flashed at 900 \( ^\circ \text{C} \). These results suggest that the flashing at 1250 \( ^\circ \text{C} \) washed out the anisotropy in the surface-state conductivity by dramatically increasing the space-charge-layer conductivity.

### 1. Temperature dependence of \( \sigma \perp \): conductivity of the space charge (subsurface) layer

![FIG. 2: (Color Online) Temperature dependence of the anisotropic conductivity of Si(111)4\times1-In formed on the substrates flashed at 1250 \( ^\circ \text{C} \) and 900 \( ^\circ \text{C} \) (Ishizaka-Shiraki method). The solid lines are for eye guide.](image-url)
form an annealing in vacuum, boron diffuses into the substrate to their results, although the space charge region of the Si values of the conductivity (at RT) near the subsurface µ the surface, it was deepened to more than 2 µm after annealed at 1300 °C. This is also confirmed from Hall effect measurements. Generally, the conductivity can be written as

$$\sigma = e n \mu,$$  \hspace{1cm} (5)

where $n$ is the carrier density, $e$ is the elementary charge and $\mu$ is the mobility. In the calculation, the carrier density for the 2 µm p-type bulk was derived assuming an acceptor concentration of $10^{16}$ cm$^{-3}$. The temperature dependence arises from the Fermi-Dirac distribution function. The mobility also shows temperature dependence and the metallic behavior from RT to ∼150 K of the sample flashed at 1250 °C can be explained by the increased mobility of carriers upon cooling. Because $n$ is almost constant in this temperature region (saturation regime), $\sigma$ reflects the behavior of $\mu$. The drastic decrease of $\sigma$ below 150 K reflects the freeze-out of $n$. This kind of behavior is actually reported by Morin et al. On the other hand, for the usual inversion-type space charge layer as shown in Fig. 3(c), by using a well-established method by solving the Poissons equation we obtained the band bending and the resulting excess carrier concentration since the $E_F$ positions at the surface and in the deep bulk are known. The decrease of $n$ happens from RT and $\sigma_{SCL}$ decreases already from RT as in the case of $\sigma_\perp$ at 900 °C flashed sample in Fig. 3(d).

2. Temperature dependence of $\sigma_\parallel$: surface-state conductivity

All of the above facts showed that $\sigma_\perp$ is dominated by the conductivity of the space charge layer or the subsurface region ($\sigma_{SCL}$) as indicated in Eq. (4), meaning negligible contribution from $\sigma_{SS}$. Now we move on to discuss $\sigma_\parallel$. As written in Eq. (3), $\sigma_\parallel$ should be the sum of $\sigma_{SS}$ and $\sigma_{SCL}$. Since $\sigma_{SCL}$ is equal to $\sigma_\perp$, we can also estimate $\sigma_{SS}$ by subtracting $\sigma_\perp$ from $\sigma_\parallel$ ($\sigma_{SS} = \sigma_\parallel - \sigma_{SCL}$).

First we discuss the behavior of $\sigma_\perp$ in more detail. From Fig. 2, we found that the values and temperature dependence of $\sigma_\perp$ rely significantly on the flashing temperature for cleaning the substrates. To gain more insight into the relation between $\sigma_\perp$ and the flashing temperature, we measured $\sigma_\perp$ at RT as a function of the flashing temperature (Fig. 3(a)). The values of $\sigma_\perp$ increased systematically by increasing the flashing temperature and changed by an order of magnitude by going from 900°C to 1350°C. To interpret this, we recall the report by Zhang et al. It is suggested that after high temperature flashing, a p-type layer is formed near the surface irrespective of the doping type of the substrate used as schematically shown in Fig. 3(b). Therefore in contrast to the simple band bending expected from the surface-core level shift (Fig. 3(c)), the conductivity of the space charge layer can change by changing the flashing temperature. This is also confirmed from Hall effect measurements, which showed a p-type Hall resistance even when an n-type substrate was used.

Liehr et al. also reported that due to high temperature annealing in vacuum, boron diffuses into the substrate to form an p-type layer near the surface. According to their results, although the space charge region of the Si substrate flashed at 1035 °C was around 100 nm below the surface, it was deepened to more than 2 µm after annealed at 1300 °C. Using these values, the estimated values of the conductivity (at RT) near the subsurface of the substrates annealed at 1035°C and 1300 °C are $4 \mu S/\square$ and $200 \mu S/\square$, respectively. These values are in reasonable agreement with our measured results of $\sigma_\perp$ for both the substrates flashed at 900 °C and 1250 °C. The reported amounts of dopant in the substrates annealed at high temperature is in the order of $10^{16}$ cm$^{-3}$, and the substrates annealed 1250 °C in our experiment would be doped to the same order of magnitude in the subsurface region.

Now we turn to the temperature dependence of the conductivity in these space-charge layers. We assume that the heavily p-doped subsurface region in Fig. 3(b) should be considered as that of a p-type bulk of ~2 µm thick. Figure 3(d) shows the measured temperature dependence of $\sigma_\perp$ and the calculated results of of an inversion-type space charge layer as shown by Fig. 3(c) for the 900 °C-flashed sample (the surface Fermi level position of the 4×1-1-In is 0.13 eV above the bulk valence-band maximum) and a p-type bulk doped in the order of $10^{16}$ cm$^{-3}$ for the 1250 °C-flashed sample. The calculated results agree almost completely with the experimental data. Generally, the conductivity can be written as

$$\sigma = e n \mu,$$  \hspace{1cm} (5)

where $n$ is the carrier density, $e$ is the elementary charge and $\mu$ is the mobility. In the calculation, the carrier density for the 2 µm p-type bulk was derived assuming an acceptor concentration of $10^{16}$ cm$^{-3}$.
plot of the different flashing temperatures and the RHEED spot intensity
dependence of the RHEED spot intensity of the
at low temperature. Shown together is the tempera-
different bulk doping) which actually showed anisotropy
thus obtained for samples prepared differently (also with
(200, 80, and 340 meV for the
has no indication of the ×8 spots at this temperature, meaning that there is no correlation of this intra-chain doubling between the neighboring chains. Then the ×2 spot intensity drastically increased at 110 K with the appearance of the ×8 spot. As for the $\sigma_{SS}$, although we see some differences, all the samples basically show the same behavior: $\sigma_{SS}$ slightly decreases from RT down to ~150 K, then the slope of the decrease becomes slightly larger below 150K, then finally it drastically decreases at 110 K which corresponds to the phase transition to the ×2 phase. Therefore, the origin of the drastic decreasing behavior of $\sigma_{SS}$ below 110 K can be attributed to the occurrence of the MI transition reported previously. The change in the decrease speed at 150 K can be related to the appearance of the 4×2 phase. Because the 4×2 structure appears around defects and the 4×1 and 4×2 phases are coexisting with the domain walls fluctuating before the appearance of the ×8 spots, there is still conductive percolation paths above 110 K. Therefore the slight increase in the decrease rate of $\sigma_{SS}$ around 150 K is likely due to the 4×1→4×2 transition. Below 110 K, such conductive percolation paths are closed by the 8×2 phase formation.

It is possible to deduce the energy gap (2\Delta) in the insulating 8×2 phase from the equation, $\sigma_{SS} \propto \exp \left(-\frac{\Delta}{k_B T}\right)$. The inset in Fig. 4(a) shows the results fitted to ln$\sigma_{SS} \propto 1/T$ which gives 2\Delta = 250 ± 30 meV. This is consistent with the gap observed in ARPES measurements (200, 80, and 340 meV for the m1, m2, and m3 bands respectively). This reconfirms that we are actually measuring the surface-state conductivity in these measurements.

However, there is one discrepancy between the present surface-state conductivity and previous ARPES measurements. In our measurement, the temperature dependence of $\sigma_{SS}$ in the 4×1 phase is non-metallic ($\sigma_{SS}$ decreases with cooling) in contrast to the metallic band structure above the MI transition temperature. The non-metallic behavior can be especially noticed in the samples flashed at 900 °C. As we have mentioned before, the values of $\sigma_{SS}$ of the sample flashed at 900 °C is lower than that of the samples flashed at 1250 °C by almost an order of magnitude. This is probably because of the defective surface, which hinders metallic conduction and lowers the conductivity itself. As discussed in Ref. 36, when the sheet conductivity is roughly below the minimum metallic conductivity ($2e^2/h \approx 80 \mu S/\square$), the temperature dependence becomes non-metallic. The Si(111)-7×7 and Si(111)-\sqrt{3}×\sqrt{3}-Sn surfaces are other examples of this. \cite{37,38} Figures 4 (b) and (c) show the RHEED patterns of the substrates flashed at 1250 °C, whereas Figs. 4 (d) and (e) are those for samples flashed at 900 °C after Ishizaka-Shiraki etching. The patterns for the 900 °C flashed samples are much weaker and more blurred than those of the 1250 °C flashed ones, showing lower surface quality. Another
measurements increases due to the pinning effect of the system makes the transition less apparent and the phase is likely another effect of defects on the surface. It has been shown previously that inducing O/H defects on this system makes the transition less apparent and the phase transition temperature determined by the conductivity measurements increases due to the pinning effect of the fluctuation by the defects.\footnote{39}

One may also notice that the phase transition has been somewhat blurred and the transition temperature seems to be increased for the samples flashed at 900 °C. This is likely another effect of defects on the surface. It has been shown previously that inducing O/H defects on this system makes the transition less apparent and the phase transition temperature determined by the conductivity measurements increases due to the pinning effect of the fluctuation by the defects.\footnote{39}

IV. CONCLUSION

In summary, we measured the temperature dependence of anisotropic surface-state conductivity of Si(111)\times 1-In using the variable-temperature four-tip STM. By the square m4PP method, we succeeded in measuring the conductivity parallel and perpendicular to In chains independently as a function of temperature and clarified the transport mechanism. We have found that the conductivity perpendicular to the In chains $\sigma_\perp$ is mainly the conductivity of the space-charge layer of the substrate, $\sigma_{SCL}$. Moreover, it was verified that $\sigma_{SCL}$ strongly depended on the flashing temperature of the substrate probably because of boron incorporation. In contrast, the conductivity parallel to In chains $\sigma_\parallel$ is mainly dominated by the surface states. The $\sigma_{SS}$ decreased drastically at 110 K due to the well-known metal-insulator transition. The LT phase ($8\times 2$) had an energy gap as large as $\sim 250$ meV, consistent with previous ARPES measurements\footnote{16}. However, the expected metallic behavior at the high-temperature phase was not detected probably due to defects on the surface.

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\bibitem{29} To avoid contamination effects from the electron beam irradiation, the SEM observation was performed only during the measurements and was turned off while waiting in the measurement of Fig. 1(b). This was actually quite important and if we turned on the SEM at all times dur-
\end{thebibliography}
ing the measurement, the decrease in $\sigma_\parallel$ was much more rapid. When we measured the temperature dependence of the conductivity, the SEM was also turned on only during the measurement and was turned off while waiting for the sample to cool down. We only directly contacted the sample with the probes for the measurements and for the rest of the time, the contact was maintained as a tunneling contact.

The bulk contribution can be neglected because of the small probe spacing and a $pn$ junction between the surface space-charge layer and the bulk, which can be confirmed by changing the substrate doping level.

33 We have shown in Ref. 20 that the metal-insulator transition cannot be measured by using a $p$-type substrate. This is because the current mainly flows through the bulk. But in the present case, although a $p$-type layer is formed in the space-charge region, the substrate itself is $n$-type. Therefore, there is a carrier depletion layer which makes the current flow confined within the surface region.